

Script generated by TTT

Title: Seidl: Virtual_Machines (15.06.2015)

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Pages: 42

A More Realistic Example

$\text{app}(X, Y, Z) \leftarrow X = [], Y = Z$
 $\text{app}(X, Y, Z) \leftarrow X = [H|X'], Z = [H|Z'], \text{app}(X', Y, Z')$
? $\text{app}(X, [Y, c], [a, b, Z])$

Remark

$[]$ == the atom **empty list**
 $[H|Z]$ == **binary** constructor application
 $[a, b, Z]$ == shortcut for: $[a|[b|[Z|[]]]]$

A program p is constructed as follows:

$t ::= a \mid X \mid _ \mid f(t_1, \dots, t_n)$
 $g ::= p(t_1, \dots, t_k) \mid X = t$
 $c ::= p(X_1, \dots, X_k) \leftarrow g_1, \dots, g_r$
 $p ::= c_1 \dots c_m ? g$

- A **term** t either is an atom, a variable, an anonymous variable or a constructor application.
- A **goal** g either is a literal, i.e., a predicate call, or a unification.
- A **clause** c consists of a **head** $p(X_1, \dots, X_k)$ with predicate name and list of formal parameters together with a **body**, i.e., a sequence of goals.
- A **program** consists of a sequence of clauses together with a single goal as **query**.

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$\text{app}([], Z, Z).$
 $\text{app}([H|X'], Y, [H|Z']) :- \text{app}(X', Y, Z').$
 $?- \text{app}(X, [Y, c], [a, b, Z]).$

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Procedural View of Proll programs:

literal == procedure call
 predicate == procedure
 clause == definition
 term == value
 unification == basic computation step
 binding of variables == **side effect**

Note: Predicate calls ...

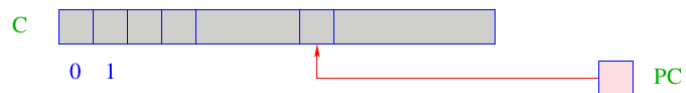
- ... do not have a return value.
- ... affect the caller through side effects only :-)
- ... may **fail**. Then the next definition is tried :-)

==> **backtracking**

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28 Architecture of the WiM:

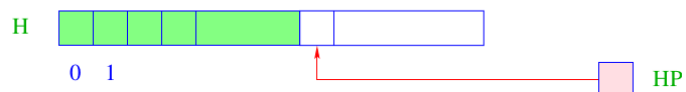
The Code Store:



- C = Code store – contains WiM program;
every cell contains one instruction;
- PC = Program Counter – points to the next instruction to executed;

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The Heap:

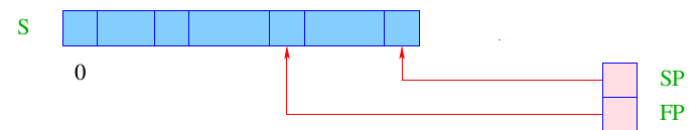


- H = Heap for dynamically constructed terms;
- HP = Heap-Pointer – points to the first free cell;

- The heap is maintained like a **stack** as well :-)
- A new-instruction allocates an object in H.
- Objects are **tagged** with their types (as in the MaMa) ...

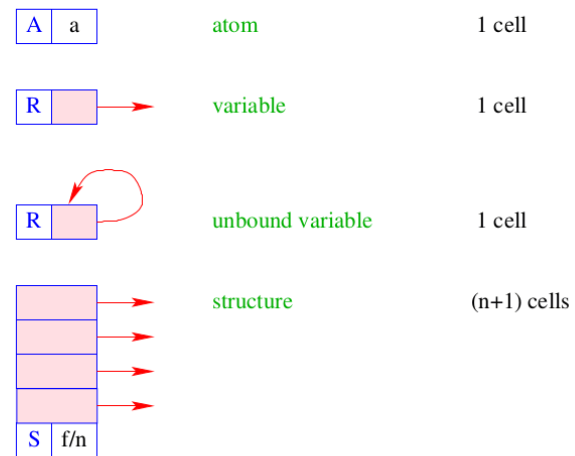
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The Runtime Stack:



- S = Runtime Stack – every cell may contain a value or an address;
- SP = Stack Pointer – points to the topmost occupied cell;
- FP = Frame Pointer – points to the current stack frame.
- Frames are created for predicate calls,
contain cells for each variable of the current clause

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29 Construction of Terms in the Heap

Parameter terms of goals (calls) are constructed in the heap before passing.

Assume that the address environment ρ returns, for each clause variable X its address (relative to FP) on the stack. Then $\text{code}_A t \rho$ should ...

- construct (a presentation of) t in the heap; and
- return a reference to it on top of the stack.

Idea

- Construct the tree during a **post-order** traversal of t
- with one instruction for each new node!

Example $t \equiv f(g(X, Y), a, Z)$.

Assume that X is **initialized**, i.e., $S[\text{FP} + \rho X]$ contains already a reference, Y and Z are not yet initialized.

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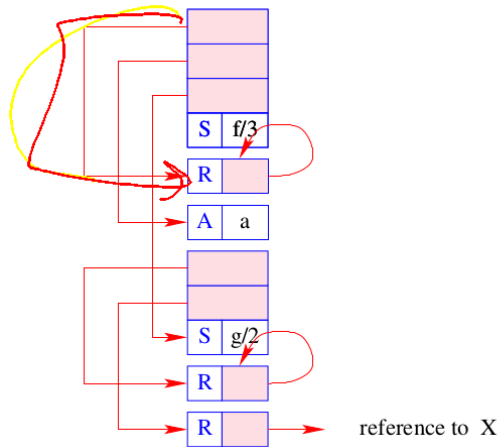
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Representing

$t \equiv f(g(X, Y), a, Z)$:



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For a distinction, we mark occurrences of already initialized variables through **over-lining** (e.g. \bar{X}).

Note: Arguments are always initialized!

Then we define:

$\text{code}_A a \rho = \text{putatom } a$
 $\text{code}_A X \rho = \text{putvar } (\rho X)$
 $\text{code}_A \bar{X} \rho = \text{putref } (\rho X)$
 $\text{code}_A _ \rho = \text{putanon}$

$\text{code}_A f(t_1, \dots, t_n) \rho = \text{code}_A t_1 \rho$
 \dots
 $\text{code}_A t_n \rho$
 $\text{putstruct } f/n$

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Then we define:

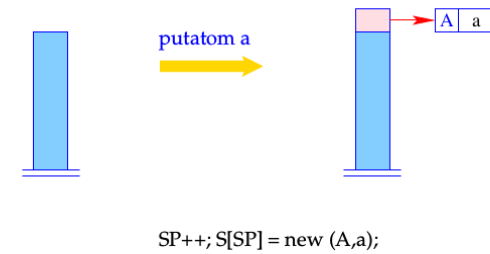
$code_A a \rho =$	$putatom a$	$code_A f(t_1, \dots, t_n) \rho =$	$code_A t_1 \rho$
$code_A \bar{X} \rho =$	$putvar (\rho X)$		\dots
$code_A \bar{X} \rho =$	$putref (\rho X)$		$code_A t_n \rho$
$code_A _ \rho =$	$putanon$		$putstruct f/n$

For $f(g(\bar{X}, Y), a, Z)$ and $\rho = \{X \mapsto 1, Y \mapsto 2, Z \mapsto 3\}$ this results in the sequence:

$putref 1$	$putatom a$
$putvar 2$	$putvar 3$
$putstruct g/2$	$putstruct f/3$

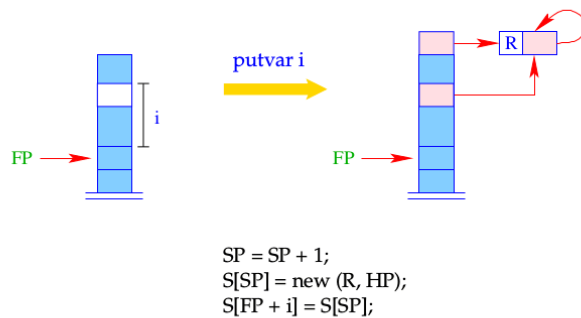
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The instruction $putatom a$ constructs an atom in the heap:



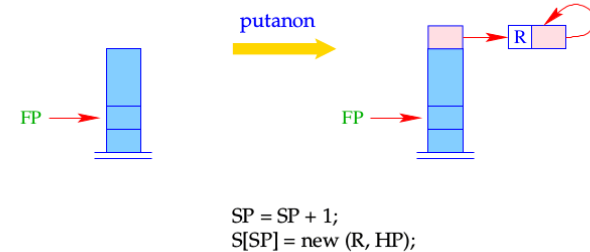
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The instruction $putvar i$ introduces a new unbound variable and additionally initializes the corresponding cell in the stack frame:



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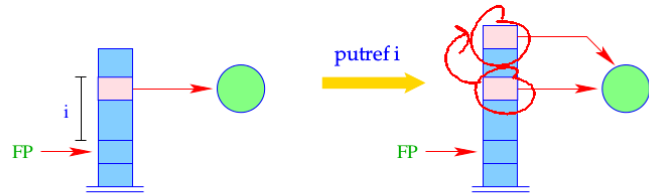
The instruction $putanon$ introduces a new unbound variable but does not store a reference to it in the stack frame:



f(-, -) = f(x, y)

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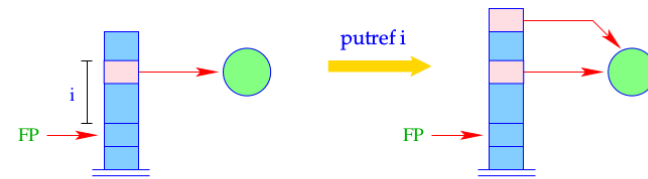
The instruction `putref i` pushes the value of the variable onto the stack:



$SP = SP + 1;$
 $S[SP] = \text{deref } S[FP + i];$

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The auxiliary function `deref` contracts chains of references:

```
ref deref (ref v) {
  if (H[v]==(R,w) && v!=w) return deref (w);
  else return v;
}
```

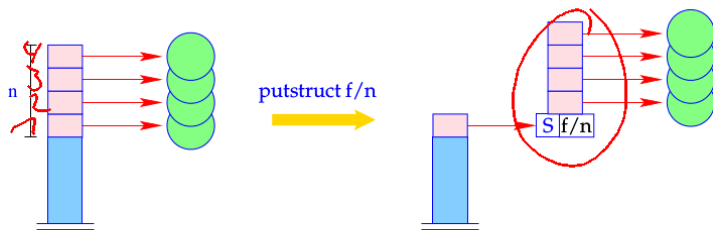
Remarks

- The instruction `putref i` does not just push the reference from $S[FP + i]$ onto the stack, but also dereferences it as much as possible
 \implies maximal contraction of reference chains.
- In constructed terms, references always point to **smaller** heap addresses.

Also otherwise, this will be often the case. Sadly enough, it cannot be **guaranteed** in general :-(<

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The instruction `putstruct f/n` builds a constructor application in the heap:



$v = \text{new } (S, f, n);$
 $SP = SP - n + 1;$
 for ($i=1; i \leq n; i++$)
 $H[v + i] = S[SP + i - 1];$
 $S[SP] = v;$

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$p(t_1, t_2)$

30 The Translation of Literals

Idea

- Literals are treated as **procedure calls**.
- We first allocate a stack frame.
- Then we construct the actual parameters (in the heap)
- ... and store references to these into the stack frame.
- Finally, we jump to the code for the procedure/predicate.

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```
code_G p(t_1, ..., t_k) ρ =  mark B           // allocates the stack frame
                             code_A t_1 ρ
                             ...
                             code_A t_k ρ
                             call p/k         // calls the procedure p/k
                             B : ...
```

Example $p(a, X, g(\bar{X}, Y))$ with $\rho = \{X \mapsto 1, Y \mapsto 2\}$

We obtain:

```
mark B           putref 1           call p/3
putatom a        putvar 2           B: ...
putvar 1         putstruct g/2
```

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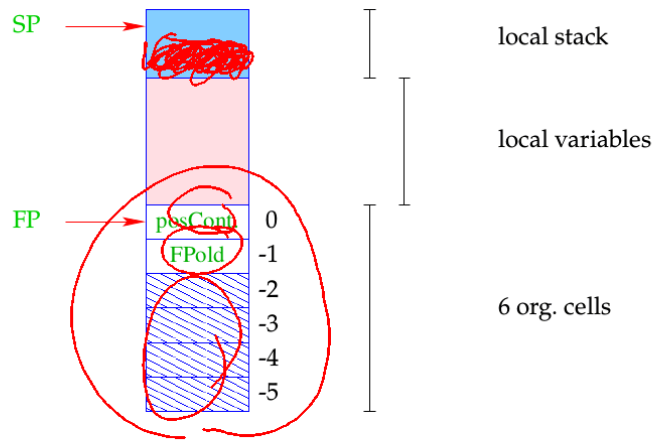
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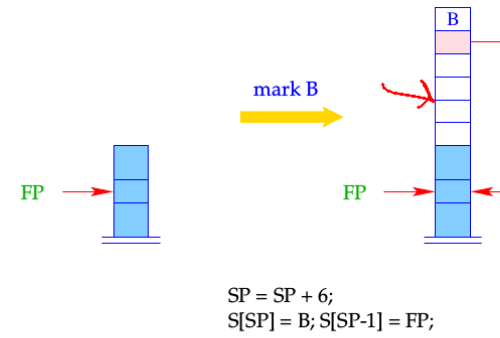
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Stack Frame of the WiM:



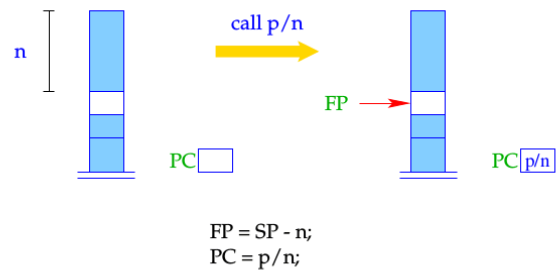
253

The instruction `mark B` allocates a new stack frame:



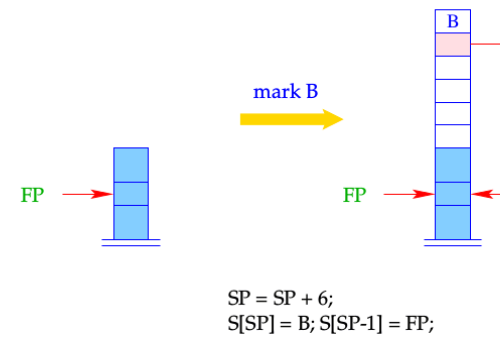
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The instruction `call p/n` calls the n -ary predicate p :



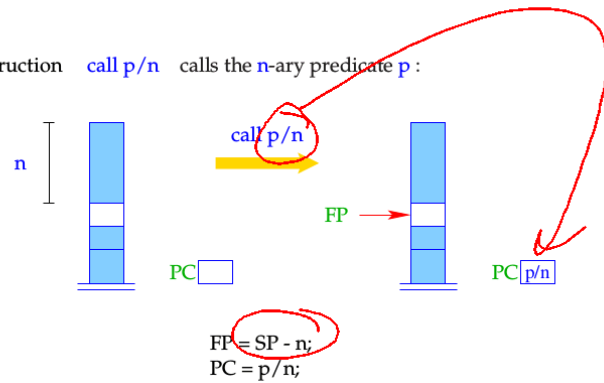
256

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Let us translate the unification $\tilde{X} = t$.

Idea 1

- Push a reference to (the binding of) X onto the stack;
- Construct the term t in the heap;
- Invent a new instruction implementing the unification :-)

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31 Unification

Convention

- By \tilde{X} , we denote an occurrence of X ; it will be translated differently depending on whether the variable is initialized or not.
- We introduce the macro `put \tilde{X} ρ` :

`put X ρ` = `putvar (ρX)`
`put $_$ ρ` = `putanon`
`put \tilde{X} ρ` = `putref (ρX)`

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`codeG ($\tilde{X} = t$) ρ` = `put \tilde{X} ρ`
`codeA t ρ`
`unify`

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Example

Consider the equation:

$$\bar{U} = f(g(\bar{X}, Y), a, Z)$$

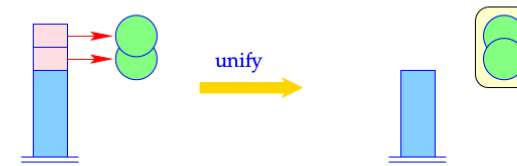
Then we obtain for an address environment

$$\rho = \{X \mapsto 1, Y \mapsto 2, Z \mapsto 3, U \mapsto 4\}$$

putref 4	putref 1	putatom a	unify
	putvar 2	putvar 3	
	putstruct g/2	putstruct f/3	

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The instruction `unify` calls the `run-time` function `unify()` for the topmost two references:



```
unify (S[SP-1], S[SP]);
SP = SP-2;
```

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The Function `unify()`

- ... takes two heap addresses.
For each call, we guarantee that these are **maximally de-referenced**.
- ... checks whether the two addresses are already **identical**.
If so, does nothing :-)
- ... binds **younger variables** (larger addresses) to **older variables** (smaller addresses);
- ... when binding a variable to a term, checks whether the variable occurs inside the term \implies **occur-check**;
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262

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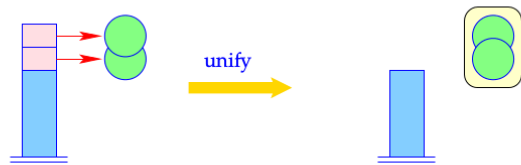
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